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Attorney's Docket No.: 14603-026US1 / P2004, 0133 US N IAP11 Rec'd PCT/PTO 10 AUG 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bernd Deutschmann, et al.

Art Unit : Unknown

Serial No.: N/A

Examiner: Unknown

Filed

: Herewith

Title

: CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN

INTEGRATED SEMICONDUCTOR CIRCUIT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: August 10, 2006

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Fish & Richardson P.C. 225 Franklin Street Boston, MA 02110

Telephone: (617) 542-5070 Facsimile: (617) 542-8906

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August 10, 2006

Date of Deposit

Substitute Form PTO-1449 (Modified)

U.S. Department of Commerce Patent and Trademark Office Attorney's Docket No. 14603-026US1

Application No.

Information Disclosure Statement by Applicant (Use several sheets if necessary)

Applicant

Bernd Deutschmann, et al.

Filing Date

Group Art Unit

(37 CFR §1.98(b))

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	6,177,298	01/23/01	John H. Quigley			
	AB	6,618,233	09/09/03	Christian Cornelius Russ et al			
	AC	5,982,601	11/09/99	Shi-Tron Lin			
	AD						
	AE						
	AF						
	AG						
	AH						
-	AI						
:	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Trans	lation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL	WO01/11685	02/15/2001	WIPO				
	AM	0 822 596	02/04/1998	EPO				
	· AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)					
Examiner	Desig.				
Initial	ID	Document			
	AQ				
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					